

APPENDIX B  
VERSION WITH MARKINGS TO SHOW CHANGES MADE  
37 C.F.R. § 1.121(b)(iii) AND (c)(ii)

CLAIMS:

3. [A delay line according to claim 2] A delay line comprising:  
a dielectric substrate including a pair of main surfaces;  
a transmission line disposed on one of the main surfaces of the dielectric substrate;  
a ground conductor disposed on the other of the main surfaces of the dielectric substrate; and  
an adjustable capacitance being disposed on the dielectric substrate and connected to the  
transmission line for setting a desired delay time of the delay line, wherein said capacitance is  
provided by a variable capacitor.
4. [A delay line according to claim 2] A delay line comprising:  
a dielectric substrate including a pair of main surfaces;  
a transmission line disposed on one of the main surfaces of the dielectric substrate;  
a ground conductor disposed on the other of the main surfaces of the dielectric substrate; and  
an adjustable capacitance being disposed on the dielectric substrate and connected to the  
transmission line for setting a desired delay time of the delay line, wherein said capacitance is  
provided by a varicap diode.
5. [A delay line according to claim 1] A delay line comprising:  
a dielectric substrate including a pair of main surfaces;  
a transmission line disposed on one of the main surfaces of the dielectric substrate;  
a ground conductor disposed on the other of the main surfaces of the dielectric substrate; and  
a capacitance being disposed on the dielectric substrate and connected to the transmission  
line for setting a desired delay time of the delay line, wherein said capacitance is provided by a  
diode.

7. [A delay line according to claim 1] A delay line comprising:  
a dielectric substrate including a pair of main surfaces;  
a transmission line disposed on one of the main surfaces of the dielectric substrate;  
a ground conductor disposed on the other of the main surfaces of the dielectric substrate; and  
a capacitance being disposed on the dielectric substrate and connected to the transmission  
line for setting a desired delay time of the delay line, wherein said capacitance is connected in  
parallel to the transmission line.

9. [A delay line according to claim 8] A delay line comprising:  
a multilayer structure formed by laminating a plurality of dielectric layers;  
a transmission line formed on a dielectric layer embedded in the multilayer structure;  
a plurality of ground conductors disposed on the dielectric layers and a pair of said ground  
conductors being disposed on opposite sides of the transmission line; and  
a capacitance disposed on the multilayer structure and connected to the transmission line for  
setting a desired delay time of the delay line, wherein said capacitance is adjustable.

13. [A delay line according to claim 8] A delay line comprising:  
a multilayer structure formed by laminating a plurality of dielectric layers;  
a transmission line formed on a dielectric layer embedded in the multilayer structure;  
a plurality of ground conductors disposed on the dielectric layers and a pair of said ground  
conductors being disposed on opposite sides of the transmission line; and  
a capacitance disposed on the multilayer structure and connected to the transmission line for  
setting a desired delay time of the delay line, wherein said capacitance is provided by a diode.